

Notice of References Cited	Application/Control No. 10/551,266	Applicant(s)/Patent Under Reexamination AKINO, TOSHIRO	
	Examiner Colleen O'Toole	Art Unit 2816	Page 1 of 1

U.S. PATENT DOCUMENTS					
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,557,231	09-1996	Yamaguchi et al.	327/534
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS						
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS		
*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Jin et al.. "On the Power Dissipation in Dynamic Threshold Silicon-on-Insulator CMOS Inverter". Aug 1998. IEEE Transactions on Electron Devices. Vol 45, No 8. Pages 1717-1724.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.